Final Review
Logistics

- **Date/Time:** Wednesday, Dec 9, 7:30pm – 9:30pm

- **Locations:**
  - ECEN-454: 1037 ETB
  - ECEN-714: 1020 ETB
  - Please go to the right room!
Logistics

- Closed book & notes
- **Two** double-sided cheat-cheat sheets (US letter/A4) are allowed
- 4-5 problems to test the understanding of important concepts covered in the course
- Read both lecture notes and the related book chapters to prepare
- Only the topics that were discussed in the class will be covered
Topics

- **Final is cumulative**
- But emphasis is given to the materials covered after the midterm
- **Highlights** of the topics after the midterm
  - Delay analysis
  - Interconnects
  - Logic effort and gate sizing
  - Combinational/sequential circuits
  - Clocking
  - Memory (Latch, FLOP, SRAM, ROM)
- **Lectures that will NOT be tested**
  - "Introduction", "Low Power Design", "Package, Power, I/O"
    (encouraged to read the lecture notes to get to know of these)

- **NO CHEATING !!**